

	Clase	Subclase
ISSUE CLASSIFICATION		

FILED UNDER 35 U.S.C. 371

**PATENT NUMBER**

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## U.S. UTILITY Patent Application

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APPLICATION NO. 09/890471	CONT/PRIOR D F	CLASS 216	SUBCLASS 00	ART UNIT 1746	EXAMINER ALANKO
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## APPLICANTS

N. Berg

**TITLE**

## Circuit board manufacturing process

PTO-2040  
12/99

ISSUING CLASSIFICATION

[illegible]

<input type="checkbox"/> <b>TERMINAL DISCLAIMER</b>	<b>DRAWINGS</b> Sheets Drwg.    Figs. Drwg.    Print Flg.			<b>CLAIMS ALLOWED</b>	
				Total Claims	Print Claim for O.G.
<input type="checkbox"/> The term of this patent subsequent to _____ (date) has been disclaimed.	_____ (Assistant Examiner)    _____ (Date)			<b>NOTICE OF ALLOWANCE MAILED</b>	
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<input type="checkbox"/> The term of this patent shall not extend beyond the expiration date of U.S. Patent No. _____	_____ (Primary Examiner)    _____ (Date)			Amount Due	Date Paid
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